

## Remarks

Claims 1 and 2 were rejected as anticipated by Ho. Claims 3 and 4 were objected to as depending on rejected based claims. Claims 5-20 were found allowable. Applicant requests reconsideration.

Applicant appreciates the examiner's thorough consideration of applicants' prior arguments. Claim 1 was rejected as anticipated by Ho, and as such Ho must contain everyone of the claimed elements. Both Ho and claimed invention use transmitter precoding and receiver filtering. However, these precoders and filters are completely different for serving different purposes. As such, the anticipation rejection is unsupported. Particularly, Ho uses precoding in combination with anti-aliasing filtering for communicating a pilot tone, whereas the claimed invention uses precoding in combination with absolute phase filtering for removing the need for differential decoding. Curiously, allowed claims 3, 5, 10, 11 and 18 all expand upon the claimed absolute phase filter, and it is not understood why all of these claims would be allowed, and claim 1 rejected, when they also recite an absolute phase filter. An explanation as to this apparent inconsistency is kindly requested.

1       The examination "disagrees with applicant's assertion that the  
2 filtered output from the filtering step in claim 1 is the estimate  
3 of the data stream, rather it is an indication of the sequences of  
4 data symbols. Even for argument sake, the limitation of the  
5 filtering step is interpreted in light of the specification,  
6 Applicant should refer to the filter disclosed in page 10 of the  
7 specification. The filter 28 as disclosed is NOTHING BUT to  
8 generate a filtered signal to be sample by the sampler 30.  
9 Therefore, the filter 14 in the system of Ho does the SAME function  
10 as that of the filtering step of claim 1."

11  
12       Therein lies the examination's miscomprehension of the claimed  
13 invention, and its relationship to Ho. There are two fundamental  
14 flaws in the examination's reasoning. The examination's statement  
15 that "The filter 28 as disclosed is nothing but to generate a  
16 filtered signal to be sample by the sampler 30.", is an unfortunate  
17 gross undercharacterization. The examination's statement that the  
18 examiner disagrees that the output of the claimed filter is an  
19 estimate of the bit stream, is an unfortunate erroneous conclusion.

20  
21       The specification teaches: "The received baseband signal  $Z_r(t)$   
22 is first filtered by the filter bank 28 that consists of  $F$  matched  
23 filters, where  $F$  is at most  $Q=2^{L-1}$  for 2-ary GMSK signaling, and at  
24 most  $P=3 \cdot [2^{L-1}]^2$  for 4-ary GMSK signaling. The filters in the

1    filter bank 28 are matched to the Laurent amplitude-modulated  
2    pulses (PAM) of the transmitted baseband GMSK signal  $Z_b(t)$ , and may  
3    be implemented as a matched filter bank or an integrate-and-dump  
4    type filter bank. The filter bank 28 provides filtered signals  
5     $r_k(t)$  for  $0 \leq k \leq F-1$ , which are sampled by the sampler 30 at every  
6    symbol time instants  $t_n=nT$  to produce discrete sample values  $r_{k,n}.$ ",  
7    and, "With the decoding states still defined as  $S_n=(d_n, d_{n-1})$ , a  $2^2$ -  
8    state  $2^3$ -branch Viterbi algorithm is again sufficient for  
9    demodulating source symbols  $\{d_n\}$  when two 2-ary matched filters are  
10   used in the filter bank 28.", and "The duration of the 4-ary  
11   amplitude modulated pulses  $\{f_k(t)\}$  is also presented in batches,  
12   that is,  $D_0=L+1$ ,  $D_1=D_2=L$ ,  $D_3=\dots=D_{11}=L-1$ . The 4-ary trellis  
13   demodulator is simplified by retaining the first  $F=1$ ,  $F=3$  or  $F=12$   
14   matched filters in the filter bank 28.", and "In both cases, with  
15   the decoding state defined as  $S_n=(d_n)$ , a  $4^1$ -state  $4^2$ -branch Viterbi  
16   algorithm is sufficient for demodulating the source symbols  $\{d_n\}$   
17   when three 4-ary matched-filters are used in the filter bank 28.",  
18   and "The function of the precoder 12 in the GMSK transmitter 10 is  
19   to precondition the symbol sequence  $\alpha_k$  as an effective reverse  
20   function of differential encoding that intrinsically results from  
21   the GMSK modulation process. The precoding produces absolute phase

1      demodulation achieved within the GMSK receiver 20. This absolute  
2      phase demodulation eliminates the need for differential decoding of  
3      matched filters 28 while providing an improvement in signal  
4      detection performance. The preferred precoding algorithms are  
5      specific to M-ary CPM signals.", and "The present invention is  
6      directed to the precoding of a data sequence into an encoded  
7      sequence of transmitted symbols, to avoid differential decoding and  
8      for improving the BER using Laurent filtering. In the preferred  
9      form, a precoder is applied to 2-ary and 4-ary symbol sets used in  
10     a GMSK transmitter having a Gaussian filter defined by respective  
11     BT products and frequency modulator modulation indices. The  
12     preferred GMSK receivers included matched filters 28, sampling 30,  
13     and Viterbi decoding 32."

14

15      One of modest skills in the art of GMSK communications would  
16      agree that the specification teaches a little more about the  
17      claimed absolute phase filter than simply being used to provide a  
18      filtered output for sampling, as the examination incorrectly  
19      asserts. As more particularly taught in the specification, the  
20      preferred absolute phase filter is a PAM Laurent filter, that is  
21      not only matched to the transmitter Gaussian modulation, but also  
22      can be implemented as a bank of PAM filters, so as to provide an  
23      absolute phase indicating the bit/symbol stream. In so doing, the  
24      filter output has an absolute phase that directly indicates the  
25      bit/symbol stream, though further decoding would tend to decrease

1 error rates in noisy channel environments. For example, in the  
2 binary case of M=1, using the principal component Laurent filter,  
3 the sampling of only the sign of the filter output provides EXACTLY  
4 the original bit stream, in the absence of noise. Surely, an exact  
5 reproduction of the original bit stream at the output of the filter  
6 is an indication of the original bit stream. In the presence of  
7 noise, system performance would suffer, and hence, as then found in  
8 claim 2, additional decoding is used to improve the bit error rate.  
9 Perhaps a realization that the sign, that is, the sign of the  
10 absolute phase of the filter output can be directly decoded by a  
11 simple hard-limited, into the bit/symbol stream, without the use of  
12 any decoding, the examiner may now fully realize how different are  
13 the two filtering processes. The claimed invention uses transmitter  
14 precoding in combination with receiver absolute phase filtering for  
15 providing a filter output having an absolute phase that indicates  
16 the original bit stream. Ho also teaches transmitter precoding and  
17 receiver filtering, but both are different vis-à-vis the claimed  
18 invention, for solving different problems. The two functions are  
19 completely different for different purposes. The purpose of  
20 precoding and PAM filtering in the present invention removes the  
21 need for differential decoding. Ho does not teach this problem nor  
22 the claimed solution.

23  
24 More specifically, claim 1 includes the limitation of  
25 filtering the continuous phase modulated (CPM) signal into a

1 sequence of filtered signals having an absolute phase for  
2 indicating the sequence of data symbols. It is the phase of the  
3 filter output signal that is sampled, and this sampled output  
4 indicates the original bit stream. While the output of filter of Ho  
5 must contain superimposed signal components that can be eventually  
6 decoded into the original bit stream by translating signal  
7 component information into a bit stream, none of those components  
8 as taught has absolute phase components indicating the bit stream,  
9 a characteristic of PAM filtering in combination with particular  
10 precoding. Rather, Ho uses a decoding process that is required to  
11 differentially translate the filtered components of the filter  
12 output into an estimate of the original bit stream, and hence Ho  
13 requires decoding, whereas the present invention of claim 1 does  
14 not. There is no decoding step in claim 1, as the sampled filter  
15 output already directly provides an indication of the bit stream.  
16 This should underscore the importance of the claimed filter  
17 providing absolute phase information for sampling. Dependent claim  
18 2, then adds in a decoding step, but this decoding step serves to  
19 improve the bit error rates in the presence of noise, and not for  
20 differential transnational decoding the filter output into an  
21 estimate of the bit stream. That is, the decoding step in claim 2  
22 is primarily used for noise reduction, not for differential  
23 translating the sampled filter outputs. The decoding process in  
24 claim 2 is not a differential decoding process. This is the primary  
25 benefit of the claimed invention.

1  
2       No where in Ho is it disclosed that the anti-aliasing filter  
3 output has an absolute phase that indicates the bit/symbol stream.  
4 Claim 1 was rejected for anticipation by Ho, yet Ho does not have  
5 an anticipatory teaching of transmitter precoding in combination  
6 with receiver absolute phase filtering used for providing a sampled  
7 filtered output having an absolute phase indicating the bit/symbol  
8 stream. At this very point of novelty, that is, the use of  
9 transmitter precoding and receiver absolute phase filtering to  
10 provide an absolute phase indicating the bit/symbol stream, Ho does  
11 not teach nor suggest this combination. Applicant respectfully  
12 requests allowance of all of the claims.

**Respectfully Submitted**

Derrick Michael Reid

**Derrick Michael Reid**

**CERTIFICATE OF MAILING**

18 I, hereby certify that this correspondence is being deposited  
19 in the United States Postal Service in an envelope with First Class  
20 full postal prepaid thereon addressed to: Commissioner of Patent,  
21 P.O. Box 1450 Alexandria, VA 22313-1450.

23 Date: December 7, 2003

Derrick Michael Reid

Derrick Michael Reid